



Tyler J. Thorp, Ph.D.

OF COUNSEL

EDUCATION

Santa Clara University School of Law
J.D. 2010

University of Washington
Electrical Engineering
Ph.D. 1999

University of Washington
Electrical Engineering
M.S. 1998

University of Washington
Electrical Engineering
B.S. 1996

INDUSTRY GROUPS

AI, ML & Robotics
Alternative Energy
Consumer Products & Services
Electrical Engineering
Semiconductors
Industrial Design
Medical Devices
Nanotechnology
Software & Internet
Telecommunications

SERVICES

Patent
IP Litigation & Enforcement
Strategic Counseling

BAR ADMISSIONS

California
Washington
United States Patent
and Trademark Office
U.S. District Court for the
Northern District of California
U.S. Court of Appeals for the
Ninth Circuit
U.S. Court of Appeals for the
Federal Circuit

BACKGROUND

Tyler's practice focuses on patent matters pertaining to electrical, mechanical, and software technologies, including developing global patent portfolios and evaluating patent portfolios in support of litigation and licensing activities. He has a passion for deeply understanding his client's technology and partnering with them to develop strong and enforceable patent portfolios that support their business objectives.

Tyler has a decade of prior engineering experience in hardware and software development. He is a named inventor on over 65 U.S. patents and was previously an Affiliate Assistant Professor in the Electrical Engineering Department at the University of Washington.

EXPERIENCE

Tyler has extensive experience handling patent matters for clients ranging from startups to large global corporations. He has advised clients regarding patent filing strategy and portfolio management, acted as in-house patent counsel for startup and early stage companies, and drafted and prosecuted U.S. and international patent applications covering innovations in computer software, cloud-based services, augmented reality, semiconductor devices and fabrication, medical devices, and consumer electronics.

Before joining Seed IP, Tyler was a partner at a boutique IP law firm in San Francisco and an in-house patent agent at SanDisk. Before practicing law, Tyler developed software for automating the design of integrated circuits and designed digital and analog circuits at SanDisk, Matrix Semiconductor (acquired by SanDisk), and Sun Microsystems. At Matrix, he helped pioneer the design and development of three-dimensional non-volatile semiconductor memories. At Sun, he developed software to synthesize dynamic circuits from a high-level hardware description into a physical layout and worked as a design engineer on the UltraSPARC III and V microprocessors.

AFFILIATIONS

Tyler is admitted to the state bars of Washington and California, and is registered to practice before the United States Patent and Trademark Office. He is admitted to practice in the United States District Court for the Northern District of California, the United States Court of Appeals for the Ninth Circuit, and the United States Court of Appeals for the Federal Circuit. Tyler is a member of the Washington State Patent Law Association (WSPLA).



SELECTED PUBLICATIONS

"Testing the Limits of Patent Exhaustion's Authorized Sale Requirement Using Current High-Tech Licensing Practices," 50 Santa Clara L. Rev. 1017 (2010)

"512-Mbit PROM with a Three-Dimensional Array of Diode/Antifuse Memory Cells," IEEE Journal of Solid-State Circuits, Nov. 2003

"Design and Synthesis of Dynamic Circuits," IEEE Transactions on VLSI Systems, Feb. 2003

"Analysis of Blocking Dynamic Circuits," IEEE Transactions on VLSI Systems, Aug. 2003

"IC Power Distribution Challenges," IEEE/ACM International Conference on Computer-Aided Design, 2001

"Design and Synthesis of Monotonic Circuits," IEEE International Conference on Computer Design, 1999

"Monotonic Static CMOS and Dual VT Technology," ACM/IEEE International Symposium on Low Power Electronics and Design, 1999

AWARDS

ABA/BNA Award for Excellence in Intellectual Property Law

Witkin Award for Academic Excellence, IP Litigation

Witkin Award for Academic Excellence, Intellectual Property and Antitrust Law